

Please carefully read the following instructions before attempting the assignment.

RULES FOR MARKING

It should be clear that your assignment would not get any credit if:

- The assignment is submitted after the due date.
- The submitted assignment does not open, or the file is corrupted.
- Strict action will be taken if the submitted solution is copied from any other student or the internet.

You should consult the recommended books to clarify your concepts, as handouts are insufficient.

You are supposed to submit your assignment in Doc or Docx format.

Any other formats like scanned images, PDF, ZIP, RAR, PPT, BMP, etc. will not be accepted.

Topic Covered:

The objective of this assignment is to assess the understanding of students about:

- Boolean Algebra and Logic Simplification
- Karnaugh Map & Boolean Expression Simplification
- Multiplexer & Demultiplexer
- The GAL16V8
- ABEL Language

Topic Covered

Lecture 18 to Lecture 30

NOTE

No assignment will be accepted via email after the due date (whether it is due to load shedding, internet malfunctioning, etc.). Hence, refrain from uploading assignments within the last hour of the deadline. It is recommended that the solution be uploaded at least two days before its closing date.

If you find any mistakes or confusion in the assignment (Question statement), please consult your instructor before the deadline. After the deadline, no queries will be entertained in this regard.

For any query, feel free to email me at:

CS302@vu.edu.pk

Question No 01	Marks (20)
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Marks (20)

You are given with a SOP based implementation of a function using GAL. You are required to generate the basic Boolean Expression of the stated circuit. Then, you need to convert that generated Boolean Expression into ABEL Expression.

